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# An LCC-LCC compensated WPT system with inherent CC-CV transition function for battery charging applications

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## Abstract

To prolong the service life of lithium batteries, the charging process is usually divided into two stages: constant current (CC) charging, and then constant voltage (CV) charging. This paper proposes an LCC-LCC compensated wireless power transfer (WPT) system, which not only has CC and CV charging characteristics but also can realize automatic CC-CV transition, omitting auxiliary circuits such as state-of-charge detection circuit and open circuit protection circuit. The proposed system initially adopts the LCC-LCC structure for CC charging and subsequently adopts the LCC-S structure for CV charging. It is simple in structure, easy to control, and overcomes the shortcomings of traditional methods. Finally, a verification experimental prototype with a rated power of 256 W is built to verify the feasibility of the proposed system.

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# Introduction

Compared with traditional wired charging, WPT technology has the advantages of safety, aesthetics, and flexibility<sup>[11]</sup>. This promising technology has been applied in various industrial fields<sup>[2,3]</sup>. Li-ion batteries are extensively employed in the above fields due to their advantages of environmental friendliness, high power density and long lifespan. Lithium batteries are lightweight and environmentally friendly, they are often used by some electrical equipment to store electrical energy. The charging sequence of first constant current (CC) charging and then constant voltage (CV) charging is the charging method that best suits the charging characteristics of lithium batteries. The existing traditional methods that enable WPT systems to achieve stable CC and CV output and their related defects are as follows:

The three closed-loop control methods of phase shift control<sup>[4,5]</sup>, frequency conversion control<sup>[6,7]</sup> and DC-DC converter control<sup>[8,9]</sup> are currently commonly used technologies to realize CC and CV outputs. However, all three methods have disadvantages. Phase shift control is difficult to achieve zero-voltage switching and approximate zero-phase-angle (ZPA) operation when the load changes greatly, resulting in increased inverter loss. Frequency conversion control technology has the possibility of frequency bifurcation, which will seriously affect the stability of the system. At the same time, this technology is also difficult to achieve ZPA operation, which will cause reactive power circulation in the system and increase losses. DC-DC converter control technology has a large number of components, high cost, and high system complexity.

The hybrid topology switching method realizes the conversion of the system's CC and CV outputs by controlling the on-off of the switches<sup>[10–13]</sup>. However, it introduces additional AC switches, corresponding drive circuits, and passive components in the system, which increases the complexity of the circuit structure.

The dual-frequency switching method is also a commonly used method to realize CC and CV outputs<sup>[14–18]</sup>. However, this method is not only difficult to design the parameters of the compensation

components, but also difficult to limit the CC and CV frequencies within the frequency range specified by the standard.

To optimize the existing CC and CV WPT systems, this paper proposes an LCC-LCC compensated WPT system, which can not only realize CC and CV outputs but also automatically complete the transition from CC to CV mode. Compared with the previous closed-loop control, hybrid topology switching, and dual-frequency switching methods, the proposed method does not require communication links, state-of-charge detection circuits, and open-circuit protection circuits. Meanwhile, the system works at a fixed frequency throughout the entire working process to ensure system stability. In addition, the system can achieve ZPA operation in CC mode and CV mode, improving the efficiency of the system. Therefore, simple structure high reliability, and low cost can be ensured in the proposed system. It is worth noting that, similar to the abovementioned hybrid topology switching method and dual-frequency switching method, the proposed method is suitable for scenarios with fixed mutual inductance, such as electric bicycle charging applications.

# **Theoretical analysis**

## Overview of the proposed LCC-LCC Compensated WPT system

The overall circuit configuration of the proposed LCC-LCC compensated WPT system with inherent CC and CV characteristics and automatic CC-CV transition function is shown in Fig. 1, which is also the circuit configuration of CC charging.  $U_D$  is the DC input voltage. The inverter comprised of four MOSFETs ( $Q_1, Q_2, Q_3, \text{ and } Q_4$ ) is applied to supply the LCC-LCC resonant tank.  $L_{P_r} L_{S_r}$  and M stand for the self-inductances of transceiver-side coils and the corresponding mutual inductance.  $L_{1, C_{P1}}$ , and  $C_{P2}$  are the compensation inductance and capacitances on the transmitter. The special receiver-side LCC resonant tank composed of the compensation inductance  $L_{2, r}$  compensation capacitances  $C_{S1}$  and  $C_{S2}$  are utilized to obtain the automatic CC-CV transition function.  $R_T, R_P, R_{S_r}$  and  $R_2$  stand for the

corresponding parasitic resistances of  $L_1$ ,  $L_p$ ,  $L_s$ , and  $L_2$ , respectively. In the initial stage of charging, the CC mode is performed, the proposed system operates in the LCC-LCC resonant tank with inherent CC characteristics, as shown in Fig. 1. As charging progresses, the battery voltage continuously increases. At the CC-CV transition point, the battery voltage is just higher than the voltage across  $C_{s1}$ , diodes  $D_1$  and  $D_2$  are forced to reverse bias. Then, the proposed system automatically converts to LCC-S resonant tank with inherent CV characteristics. The associated circuit configuration of the proposed system for CV charging is depicted in Fig. 2.

## **Analysis of CV mode**

In CV mode, since the battery voltage is higher than the voltage across  $C_{S1}$ , diodes  $D_1$  and  $D_2$  are forced to reverse bias, breaking the branch where  $L_2$  is located. The proposed system operates in LCC-S topology to perform CV charging, as shown in Fig. 2. The corresponding simplified circuit diagram and equivalent circuit diagram are shown in Fig. 3a & b, respectively. According to Kirchhoff's voltage law (KVL), Eqn (1) can be obtained.

$$U_{i} = \left(j\omega L_{1} + \frac{1}{j\omega C_{P1}}\right)I_{1} - \frac{1}{j\omega C_{P1}}I_{2}$$

$$0 = \left(j\omega L_{P} + \frac{1}{j\omega C_{P1}} + \frac{1}{j\omega C_{P2}}\right)I_{2} - \frac{1}{j\omega C_{P1}}I_{1} + j\omega MI_{3}$$

$$0 = \left(j\omega L_{S} + \frac{1}{j\omega C_{S}} + R_{E2}\right)I_{3} + j\omega MI_{2}$$
(1)

To simplify the analysis, the LCC-S topology should satisfy the following resonance conditions.

$$\begin{cases}
j\omega L_{1} + \frac{1}{j\omega C_{P1}} = 0, j\omega L_{P} + \frac{1}{j\omega C_{P1}} + \frac{1}{j\omega C_{P2}} = 0 \\
j\omega L_{S} + \frac{1}{j\omega C_{S}} = 0, \frac{1}{C_{S}} = \frac{1}{C_{S1}} + \frac{1}{C_{S2}}
\end{cases}$$
(2)

Substituting Eqn (2) into Eqn (1), Eqn (3) can be obtained.

$$\begin{cases} I_1 = \frac{M^2 U_i}{L_1^2 R_{E2}} \\ U_{BO} = I_3 R_{E2} = -\frac{M U_i}{L_1} \\ Z_{in} = \frac{U_i}{I_1} = \frac{L_1^2 R_{E2}}{M^2} \end{cases}$$
(3)

Combining Eqn(3) and  $U_{BO} = (\sqrt{2}/\pi)U_B$ , the charging voltage  $U_B$  can be expressed as:

$$U_{\rm B} = \frac{\pi M U_{\rm i}}{\sqrt{2}L_1} \tag{4}$$

where,  $U_i$  is the root mean square (RMS) value of  $U_i$ . Combining Eqn (4) and  $U_i = (2\sqrt{2}/\pi)U_D$ , the charging voltage  $U_B$  can be further derived as:

$$U_{\rm B} = \frac{2MU_{\rm D}}{L_1} \tag{5}$$

Observing Eqns (3) & (5), the input impedance  $Z_{in}$  is purely resistive and the charging voltage  $U_B$  is irrelevant to the load resistance. Therefore, the system can realize load-independent CV charging and ZPA operation. In addition, the voltage gain can be adjusted by changing the parameter of the compensation inductor  $L_1$  to match the actual application requirements.

#### Analysis of CC mode

Due to the special circuit structure on the receiver, diodes  $D_1$  and  $D_2$  form one half-bridge rectifier, while diodes  $D_3$  and  $D_4$  form another half-bridge rectifier. Observing Fig. 1, the two half-bridge rectifiers have the same output terminals to power the load. Therefore, the two half-bridge rectifiers are connected in parallel. In CC mode, both the half-bridge rectifiers are activated. For the halfbridge rectifier composed of  $D_1$  and  $D_2$ , the relationship between the RMS value of the input voltage  $U_{\rm AO}$  and the output voltage  $U_{\rm B}$ is  $U_{\rm AO} = (\sqrt{2}/\pi)U_{\rm B}$ ; while for the half-bridge rectifier composed of  $D_3$  and  $D_4$ , the relationship between the RMS value of the input voltage  $U_{\rm BO}$  and the output voltage  $U_{\rm B}$  is  $U_{\rm BO} = (\sqrt{2}/\pi)U_{\rm B}$ . Therefore, in CC mode,  $U_{AO} = U_{BO}$ . To simplify the circuit analysis, the parasitic resistances  $R_T$ ,  $R_P$ ,  $R_S$ , and  $R_2$  are ignored. The simplified circuit of the LCC-LCC compensated WPT system for CC charging is depicted in Fig. 4a.  $R_{E1}$  is the equivalent resistance between the A and O terminals,  $R_{E2}$  is the equivalent resistance between the B and O terminals. Also as the two half-bridge rectifiers are connected in parallel, the relationship between  $R_{E1}$ ,  $R_{E2}$ , and  $R_{B}$  can be expressed as:

$$\frac{1}{R_{\rm E1}} + \frac{1}{R_{\rm E2}} = \frac{\pi^2}{2R_{\rm B}} \tag{6}$$

To make the CC analysis process easier to understand, the corresponding equivalent circuit diagram is provided, as shown in Fig. 4b. Assuming that  $C_{S1}$  and  $L_2$  represented in gold satisfy the resonance condition  $j\omega L_2 + 1/j\omega C_{S1} = 0$ ,  $C_{S1}$  and  $L_2$  are equivalent to being



Fig. 1 Overall circuit configuration of proposed WPT system and circuit configuration of the LCC-LCC compensated WPT system for CC charging.



Fig. 2 Circuit configuration of the LCC-S compensated WPT system for CV charging.



**Fig. 3** Simplified circuit diagram and equivalent circuit diagram in CV mode. (a) Simplified circuit diagram. (b) Equivalent circuit diagram.

short-circuited. Therefore, the part enclosed by the blue curly brackets in Fig. 4b is equivalent to  $C_{S2}$ . Through the above analysis, the proposed LCC-LCC topology can be equivalent to the cascade of the LCC-S topology and the special T-network. Among them, the output voltage of the LCC-S topology is also the input voltage of the special T-network, which is set as  $U_{T}$ . In addition, the output impedance of the LCC-S topology is also the input impedance of the special T-network, denoted as  $Z_{T}$ .

Based on the analysis of the LCC-S topology for CV charging in section Analysis of CV mode, the following Eqn (7) can be concluded:

$$\begin{cases} U_T = -U_{BO} = \frac{MU_i}{L_1} \\ Z_{in} = \frac{L_1^2 Z_T}{M^2} \end{cases}$$
(7)

Then, the special T-network in Fig. 4b is analyzed. According to KVL, Eqn (8) can be obtained:

Combining  $U_{AO} = U_{BO}$ ,  $j\omega L_2 + 1/j\omega C_{S1} = 0$ , Eqns (6) & (8), Eqn (9) can be derived:

$$\begin{cases} I_3 = -\frac{2R_{\rm B}U_{\rm T}}{\pi^2 \omega^2 L_2^2} \\ I_4 = -\frac{j(\omega L_2 \pi^2 - 2R_{\rm B})U_{\rm T}}{\pi^2 \omega^2 L_2^2} \\ Z_{\rm T} = \frac{U_{\rm T}}{-L_2} = \frac{\pi^2 \omega^2 L_2^2}{2R_2} \end{cases}$$
(9)

From Eqn (9), the equivalent output current  $I_0$  of the special Tnetwork, which is also the RMS value of the two half-bridge rectifiers' equivalent input current, can be deduced as:

$$I_{\rm O} = I_4 + I_3 = \frac{U_{\rm T}}{\omega L_2}$$
(10)

According to Eqn (10), it can be concluded that a CV source can be transformed into a CC source through the special T-network shown in Fig. 4b. Further, combining Eqns (7), (9), (10) and  $U_{\rm i} = (2\sqrt{2}/\pi)U_{\rm D}$ , the charging current  $I_{\rm B}$  and the input impedance  $Z_{\rm in}$  can be calculated as:

$$\begin{cases} I_B = \frac{\sqrt{2}}{\pi} I_O = \frac{4MU_D}{\pi^2 \omega L_1 L_2} \\ Z_{in} = \frac{\pi^2 \omega^2 L_1^2 L_2^2}{2M^2 R_B} \end{cases}$$
(11)

As evident from Eqn (11), the input impedance  $Z_{in}$  is purely resistive and the charging current  $I_B$  is irrelevant to the load resistance. Therefore, the system can achieve load-independent CC charging and ZPA operation. In addition, the transconductance gain can be adjusted by changing the parameters of compensation inductors  $L_1$  and  $L_2$  to match the actual application requirements.

### **Experimental validation**

#### **Experimental prototype**

To verify the practicability of the proposed method, an experimental prototype is fabricated, as shown in Fig. 5. Given an electric bicycle on-board battery pack with nominal voltage of 72 V and capacity of 20 Ah, the required charging current  $I_{\rm B}$  in CC mode and charging voltage U<sub>B</sub> in CV mode can be set to 3.2 A and 80 V, respectively. Four MOSFET (IRF530) are selected to form the inverter, and four diodes (MBR16100CT) are chosen to construct the rectifier. The feasibility of adopting a resistive load to replace the battery has been experimentally demonstrated in detail in a study by Chen et al.<sup>[19]</sup>. Thus, this study utilizes the resistive load to conduct experimental testing. The operating frequency f is set to 85 kHz. A DC input voltage U<sub>D</sub> of 40 V is applied to supply the system. Subsequently, through the above theoretical analysis, the parameters of each compensation component can be set reasonably by Eqns (2), (5), (11) and  $j\omega L_2 + 1/j\omega C_{S1} = 0$ . The specific circuit parameters of the proposed LCC-LCC compensated WPT system are listed in Table 1.

#### **Experimental results**

In the initial stage of charging, the proposed system performs the CC charging with an LCC-LCC resonant tank. The measured







Fig. 5 Experimental prototype.

Parameters	Value	Parameters	Value	Parameters	Value
UD	40 V	L <sub>P</sub>	100.3 <i>u</i> H	C <sub>P1</sub>	175.4 <i>n</i> F
U <sub>B</sub>	80 V	Ls	100.1 <i>u</i> H	C <sub>P2</sub>	34.9 <i>n</i> F
I <sub>B</sub>	3.2 A	$L_1$	19.8 <i>u</i> H	C <sub>S1</sub>	365.2 <i>n</i> F
f	85 kHz	L <sub>2</sub>	9.6 <i>u</i> H	C <sub>S2</sub>	38.8 <i>n</i> F
М	20.4 <i>u</i> H	R <sub>P</sub>	0.12 Ω	R <sub>S</sub>	0.1 Ω

experimental waveforms in CC mode when the battery equivalent resistance  $R_{\rm B}$  is about 5 and 10  $\Omega$  are shown in Fig. 6a & b, respectively. Observing the two figures marked with Oscilloscope 1, the charging current  $l_{\rm B}$  roughly maintains a constant value of 3.2 A when  $R_{\rm B}$  varies, which demonstrates the load-independent CC output characteristic. In addition,  $U_{\rm i}$  and  $l_{\rm 1}$  are basically in phase, indicating that the system can achieve ZPA operation in CC mode. Then, from the two figures marked with Oscilloscope 2, the RMS values of the two rectifiers' input voltages  $U_{\rm AO}$  and  $U_{\rm BO}$  are equal, which verifies the correctness of the two rectifiers working in parallel in CC mode.

In addition, as  $R_{\rm B}$  increases,  $I_4$  gradually decreases and  $I_3$  gradually increases.  $I_3$  and  $I_4$  are rectified and output in parallel to form a CC source. Furthermore, from the two figures marked with Oscilloscope 3, the battery voltage  $U_{\rm B}$  is lower than the peak value of  $U_{\rm CS1}$ (the voltage across  $C_{\rm S1}$ ) in CC mode. Diodes  $D_1$  and  $D_2$  are both in the conduction state, forming the current  $I_4$ .

As charging progresses, when  $U_{\rm B}$  rises close to the peak value of  $U_{\rm CS1}$ , the CC-CV transition is automatically performed. The measured experimental waveforms at the CC-CV transition point when  $R_{\rm B}$  is about 25  $\Omega$  are shown in Fig. 7. Observing the figure marked with Oscilloscope 1,  $U_{\rm B}$ , and  $I_{\rm B}$  are slightly lower than preset values and the ZPA operation can be achieved. Then, from the figure marked with Oscilloscope 2,  $U_{\rm AO}$  and  $U_{\rm BO}$  are approximately equal.  $I_{\rm 4}$  is very small, which means the branch where  $L_2$  is located is about to be cut off. Furthermore, from the figure marked with Oscilloscope 3,  $U_{\rm B}$  is close to the peak value of  $U_{\rm CS1}$ , diodes  $D_1$ , and  $D_2$  are both in an extremely weak conduction state, forming the weak current  $I_4$ , and the system is about to transition from LCC-LCC topology for CC charging to LCC-S topology for CV charging.

As the battery voltage continues to rise when  $U_{\rm B}$  is higher that the peak value of  $U_{CS1}$ , diodes  $D_1$  and  $D_2$  are forced to reverse bias. The branch where  $L_2$  is located is completely cut off. Then, the proposed system performs the CV charging with LCC-S resonant tank. The measured experimental waveforms in CV mode when  $R_{\rm B}$  is about 70 and 140  $\Omega$  are shown in Fig. 8a & b, respectively. Observing the two figures marked with Oscilloscope 1, the charging voltage  $U_{\rm B}$  roughly maintains a constant value of 80 V when R<sub>B</sub> varies, which demonstrates the load-independent CV output characteristic. The phase of  $I_1$  is approximately consistent with that of  $U_{ii}$ , which indicates that the system can achieve ZPA operation in CV mode. Then, from the two figures marked with Oscilloscope 2, as diodes  $D_1$  and  $D_2$  are reverse biased to break the branch where  $L_2$  is located, the current  $I_4$ drops to zero. Only the rectifier composed of  $D_3$  and  $D_4$  supplies power to the load. In addition, since the branch where  $L_2$  is located is cut off,  $U_{AO}$  and  $U_{CS1}$  will remain the same, which can be found by



**Fig. 6** Measured experimental waveforms in CC mode when the battery equivalent resistance  $R_B$  is about (a) 5  $\Omega$  and (b) 10  $\Omega$ , respectively.



Fig. 7 Measured experimental waveforms at the CC-CV transition point when the battery equivalent resistance  $R_{\rm B}$  is about 25  $\Omega$ .



Fig. 8 Measured experimental waveforms in CV mode when the battery equivalent resistance  $R_B$  is about (a) 70  $\Omega$  and (b) 140  $\Omega$ , respectively.

comparing the figures marked with Oscilloscopes 2 and 3. Furthermore, from the two figures marked with Oscilloscope 3, the currents flowing through diodes  $D_1$  and  $D_2$  are both 0, which is consistent with the current  $l_4$ , verifying that the branch where  $L_2$  is located is completely cut off. In addition, as  $R_B$  increases, the peak value of  $U_{CS1}$  decreases gradually, and the system will operate normally with LCC-S topology for CV charging until the end of charging.

Transient results with  $R_{\rm B}$  step changes from 5 to 10  $\Omega$  in CC mode and from 70 to 140  $\Omega$  in CV mode are tested respectively, as shown in Fig. 9. From Fig. 9, when  $R_{\rm B}$  is suddenly increased by 100%, the resulting changes in both charging current  $I_{\rm B}$  in CC mode and the charging voltage  $U_{\rm B}$  in CV mode are very small. In addition, there is no significant overshoot when  $R_{\rm B}$  is changed suddenly in both CC and CV modes. This further validates that the proposed system can perform CC and CV charging stably. Figure 10 shows the DC-DC efficiency curve of the proposed system during the whole charging process. In CC mode, as  $R_{\rm B}$  varies from 10 to 25  $\Omega$ , the system efficiency climbs up from 88.8% to 92.1%. In CV mode, as  $R_{\rm B}$  varies from 25 to 250  $\Omega$ , the system efficiency slowly drops from the peak of 93.38% to 88.9%. The proposed system maintains high efficiency throughout the charging process.

To reflect the low cost of the proposed system more intuitively, the configuration and cost of the proposed system are compared with currently popular hybrid topology switching and dual-frequency switching methods, as shown in Table 2. Considering hardware requirements such as detection circuits for CC-CV transition, communication links, open-circuit protection circuits, and excess components and AC switches and the corresponding driving circuits, the proposed system performs best from the perspective of cost-effectiveness.



Fig. 9 Transient results with load step changes (a) from 5 to 10  $\Omega$  and (b) from 70 to 140  $\Omega$ .



**Fig. 10** DC-DC efficiency curve of the proposed system during the whole charging process.

 Table 2.
 System configuration and cost comparison with existing popular methods

Proposed in	Ref. [10–13]	Ref. [14–18]	This study
Method	Hybrid topology switching	Dual- frequency switching	Automatic CC- CV transition
Without detection circuits	No	No	Yes
Without communication links	No	No	Yes
Without open-circuit protection circuits	No	No	Yes
Without excess components and switches	No	Yes	Yes
Cost	High	Medium	Low

# Conclusions

In this paper, an LCC-LCC compensated WPT system with inherent CC and CV characteristics and automatic CC-CV transition function are put forward for battery charging applications. CC and CV characteristics under the corresponding circuit configurations are analyzed in detail. The feasibility of the proposed system is proved by building a confirmatory prototype, and high efficiency is maintained throughout the charging process. The features of the proposal can be summarized as follows:

(1) Compared with the closed-loop control methods, the proposed system does not need to introduce complicated control algorithms, which reduces the difficulty of controller design and is easy to implement.

(2) Compared with the hybrid topology switching method, the proposed system does not require additional passive components, AC switches, and corresponding driving circuits, which simplifies the system structure.

(3) Compared with the dual-frequency switching method, the proposed system runs at a fixed frequency, which avoids the frequency bifurcation phenomenon and has high robustness.

(4) In addition, the proposed system can automatically realize the transition from CC to CV mode through its structural properties,

without additional detection circuits, communication links, and open-circuit protection circuits, which makes the system simpler and less costly.

## **Author contributions**

The authors confirm contribution to the paper as follows: study conception and design: Zhou X; data collection: Wang Y; analysis and interpretation of results: Zhou X, Yang L; draft manuscript preparation: Zhou X, Wang Y. All authors reviewed the results and approved the final version of the manuscript.

## **Data availability**

All data generated or analyzed during this study are included in this published article.

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# **Conflict of interest**

The authors declare that they have no conflict of interest.

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