

WIRELESSLY POWERING: THE FUTURE

Impedance matching approach of L-section circuit with ohmic loss in reactive components

SATOSHI SUZUKI¹, QIAOWEI YUAN² AND QIANG CHEN¹

Impedance matching is very important to improve transmission efficiency not only for wireless communication but also for wireless power transfer. Lumped reactive elements are usually used in the impedance matching circuit. These reactive components such as inductors and capacitors have ohmic loss. An exact approach to design the lumped matching circuit at the presence of the ohmic loss is derived in this paper. Moreover, the condition for selection of impedance matching topology is deduced not only for lossless case but also for lossy case. Finally, the effect of the ohmic loss in the impedance matching circuit on the transmission efficiency is demonstrated quantitatively.

Keywords: Wireless Power Transfer, Impedance matching, Ohmic loss, Q factor, Transmission efficiency

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I. INTRODUCTION

Wireless power transfer (WPT) technology has become more and more important again because of its potential application to any electronic devices without using cord or battery [1, 2]. The maximum efficiency and optimum impedances based on the conjugate image impedance conditions have been formulated by the current authors [3, 4] and other researchers [5–9]. Moreover, a calculator called as E-WPT has been developed by one of the authors [10]. E-WPT can calculate the maximum efficiency and optimum impedances using S-parameters or Z-parameters for any transmitting and receiving elements with arbitrary structures at arbitrary frequencies. To design the optimum impedances is nothing but to introduce impedance matching circuits. Impedance matching circuit reduces the undesired reflection between two unmatched impedances, thus the impedance matching circuit makes the transmission efficiency to be the maximum for wireless communication system but can also be beneficial for WPT system [11–14].

A lot of WPT application systems so far have been operated at kHz or MHz and used the electrically small antennas in the near-field coupling scenarios to prevent the undesired far-field radiation. Electrically small antenna means the physical size of the antenna is much smaller than the one wavelength at operating frequency. Lumped matching circuits such as L-section, T-section, and Π -section are very useful to be the matching

circuits for kHz or MHz WPT applications [15, 16]. Until now, lumped matching circuits have been designed theoretically by using ideal inductors and capacitors without considering their intrinsic ohmic loss [16].

However, the ohmic loss of impedance matching circuit cannot be ignored when the two impedances are quite different from each other which happens on the near-field coupling WPT scenarios [4, 17–19], where the impedance of the electrically small size of antenna usually has very small resistance and large reactance, and is thus quite different from the target impedance such as source impedance of 50 Ω .

In [17–19], the simulation results showed that the ohmic losses from the inductors and capacitors in L-section matching circuit dropped the efficiency of WPT system greatly. There are two reasons for the efficiency reduction; one is the mismatching caused by the ohmic loss because the initial impedance matching circuit was designed without consideration of the ohmic loss included in the lumped reactive elements. The other is the power consumption by the ohmic loss itself in the reactive elements.

The matching circuit design approach discussed in this paper is a fundamental one and it can be applied to any two arbitrary different impedances to be connected. This paper mostly focuses on how to design an exact L-section matching circuit based on conjugate matching condition even with the presence of the ohmic loss in reactive elements.

The novel point of this research is to give the new exact approach to design a lossy lumped matching circuit. The paper will show the exact expressions for the lossy lumped elements satisfied with the conjugate matching circuit, and also present the effect of the ohmic loss on the transmission efficiency between two impedances. The ohmic losses in the reactive components are expressed by using their Q-factors in the paper. Because the L-section composed of two reactive

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elements is a fundamental one among the lumped matching circuits, only the method for L-section matching circuit is presented, but certainly, the proposed method can be easily extended to T-section and Π -section matching circuit.

This paper is organized as follows. In Section II, a quite simple approach to obtain L-section matching circuit [8] is briefly reviewed, then the boundaries of selecting L-section matching circuit topologies in Smith Chart are presented and also completely compared with those described in [16]. An exact approach to obtain L-section matching circuit with the lossy element is presented in Section III by using the Q-factors of the practical reactive components. In Section IV, two features about the proposed lossy matching approach are discussed. One feature is about the boundaries for matching circuit topologies' selection in Smith Chart and another is the relationship between two synthesized impedances at two sides of the matching circuit. In Section V, in order to confirm the effectiveness of our proposed matching approach (PMA), the efficiencies of some examples by using PMA and conventional matching approach (CMA) are compared.

II. EXACT DESIGN APPROACH OF LOSSLESS L-SECTION MATCHING CIRCUIT

The matching circuit design method without a loss is well known. For simplicity but without loss of generality, the target impedance Z_s is assumed to have only resistive part that is $Z_s = R_s$, but the load impedance Z_l is usually a complex one, $Z_l = R_l + jX_l$. The impedance matching circuit is designed to satisfy the complex conjugate matching condition of $Z_l^{mat} = Z_s^* = R_s$, where Z_l^{mat} is the synthesized impedance at the target side of the matching circuit as shown in Fig. 1. L-section matching circuit with one shunt component and one series component has two available topologies as shown in Fig. 1, they are BX topology and XB topology, respectively. In this paper, the susceptance B designates a shunt component and reactance X is used as a series component. Positive B and negative X mean the components are capacitors, and others are inductors.

The selecting conditions for the topologies can be represented by the relationship between the impedances of Z_l and Z_s , and also the relationship between the admittances of $Y_l = 1/Z_l = G_l + jB_l$ and $Y_s = 1/Z_s = G_s$. They are expressed in the following,

BX type is selected when $G_l < G_s$

XB type is selected when $R_l < R_s$

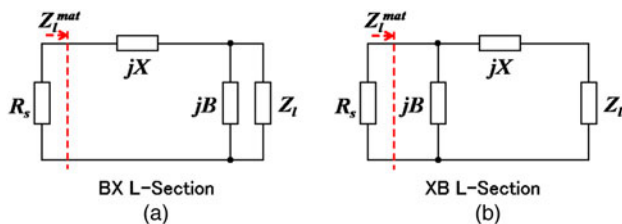


Fig. 1. L-section matching circuit.

As shown in Fig. 2, Smith Chart is divided into three areas according to the magnitude relationship between G_l and G_s , or R_l and R_s . When $G_l < G_s$, the load admittance is located in the area I and area III, when $R_l < R_s$, the load impedance is located in the area II and area III in Smith Chart, respectively. The two dotted circles which denote $R_l = R_s$ and $G_l = G_s$, respectively, are the boundaries to determine the topologies of matching circuits. BX L-section can be only effective when the load impedance is located in area I, while XB L-section can be only useful when the load impedance is located in area II. However, both BX and XB type matching circuits can be applied to these load impedances which are located inside of area III.

In case of $G_l < G_s$, BX type is selected. The susceptance B and reactance X are deduced by the matching condition exactly as

$$B = -B_l \pm \sqrt{G_l G_s - G_l^2}, \quad (1)$$

$$X = \pm \frac{\sqrt{G_l G_s - G_l^2}}{G_l G_s}. \quad (2)$$

In the same way, for the case of $R_l < R_s$, XB type is selected. B and X can be deduced as

$$X = -X_l \pm \sqrt{R_l R_s - R_l^2}, \quad (3)$$

$$B = \pm \frac{\sqrt{R_l R_s - R_l^2}}{R_l R_s}. \quad (4)$$

Equations (1), (3) and (2), (4) are compatible because if B , X , G are replaced by corresponding X , B , R , equations (1) and (2) will be equations (3) and (4). This compatibility makes the program code to obtain any L-section matching circuit very easy to implement.

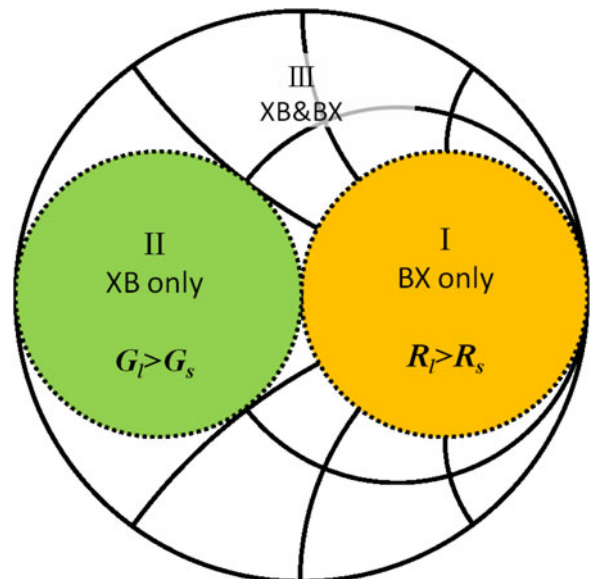


Fig. 2. L-section topology depending on Z_s and Z_l position in Smith chart.

III. PROPOSED EXACT DESIGN APPROACH OF LOSSY L-SECTION MATCHING CIRCUIT

In the practical case, inductors and capacitors have intrinsic ohmic loss in addition to their reactive components. Here, an exact approach to design a L-section matching circuit with ohmic loss is presented. Let Q_B and Q_X represent the ohmic losses in B and X , respectively, those are

$$Q_B = |B|/G, \Rightarrow G = |B|/Q_B \quad (5)$$

$$Q_X = |X|/R, \Rightarrow R = |X|/Q_X \quad (6)$$

where R represents the ohmic loss of X , while G represents the ohmic loss of B , respectively.

For the case of $G_l < G_s$, BX L-section matching is selected. From the matching condition, the following equations with unknowns B and X are established,

$$\frac{|X|}{Q_X} + \frac{G_l + |B|/Q_B}{(G_l + |B|/Q_B)^2 + (B_l + B)^2} = R_s, \quad (7)$$

$$X = \frac{B_l + B}{(G_l + |B|/Q_B)^2 + (B_l + B)^2}. \quad (8)$$

Usually, Q_X and Q_B can be estimated from the datasheets of the products, thus, B and X can be obtained exactly by solving equations (7) and (8).

Similarly, in case of $R_l < R_s$, XB L-type is selected, the following equations with unknowns B and X are obtained from the matching condition,

$$\frac{|B|}{Q_B} + \frac{R_l + |X|/Q_X}{(R_l + |X|/Q_X)^2 + (X_l + X)^2} = G_s, \quad (9)$$

$$B = \frac{X_l + X}{(R_l + |X|/Q_X)^2 + (X_l + X)^2}, \quad (10)$$

again, both B and X can be obtained exactly by solving equations (9) and (10).

IV. FEATURES OF LOSSY L-SECTION MATCHING CIRCUIT

This section investigates the differences between CMA and PMA. Subsection (A) focuses on selecting boundaries of the matching circuit topologies for different impedances. Subsection (B) focuses on the synthesized impedances Z_l^{mat} and Z_s^{mat} at two sides of the matching circuit.

A) Boundaries of selecting condition for matching circuit topology

Without the ohmic loss of a reactive component, the circles $\overline{R}_l = (R_l/R_s) = 1$ and $\overline{G}_l = (G_l/G_s) = 1$ in Smith Chart are the boundaries of area I, area II, and area III. They are

shown in Fig. 3 as dotted lines. Without ohmic loss, only BX topology can be selected as the matching circuit for the load impedances in area I while only XB topology can be applied to area II, and both BX and XB topologies are capable of being used as the matching circuits in area III. However, if the reactive components used in the matching circuit have some ohmic losses, the boundaries of area I and area II are changed from dotted lines to the dashed lines as shown in Fig. 3. The shape and location of dashed lines depend on the Q_X in case of BX L-section, and Q_B in case of XB L-section. It is because from equation (7), R_s seems to be decreased by $|X|/Q_X$ if $|X|/Q_X$ is moved from the left side to the right side of equation (7), and similarly, from equation (9) G_s seems to be decreased by $|B|/Q_B$ if $|B|/Q_B$ is moved from the left side to the right side of equation (9). The two dashed lines will be moved outside from the $\overline{R}_l = (R_l/R_s) = 1$ and $\overline{G}_l = (G_l/G_s) = 1$ when Q_X in BX L-section and Q_B in case of XB L-section become small. Small Q_X and Q_B mean the losses of the reactive components are large. The dashed line will return to the circles $\overline{R}_l = (R_l/R_s) = 1$ or $\overline{G}_l = (G_l/G_s) = 1$ when Q_X in BX L-section and Q_B in case of XB L-section get large enough. The change of boundary conditions with ohmic losses of reactive components means the impedance located in area III will be inside area I or area II which will happen for the small dipoles and the coils coupling WPT scenarios as used as the practical examples in next section.

B) Synthesized impedances Z_l^{mat} and Z_s^{mat} at two sides of the matching circuit

When a load impedance Z_l is matched to target impedance of Z_s by using lossless matching circuit as shown in Fig. 4, the synthesized impedance Z_l^{mat} at the target impedance side of matching circuit, otherwise looking into the matching circuit and Z_b , is certainly satisfied with the matching

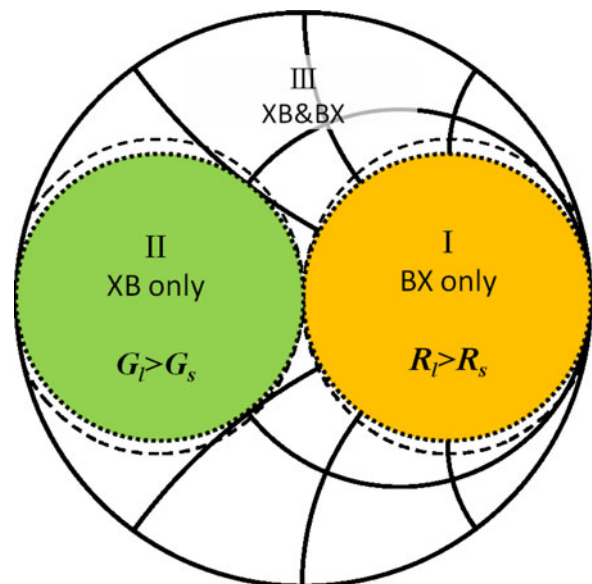


Fig. 3. Boundaries of CMA and PMA to select topologies of the matching circuit in Smith Chart.

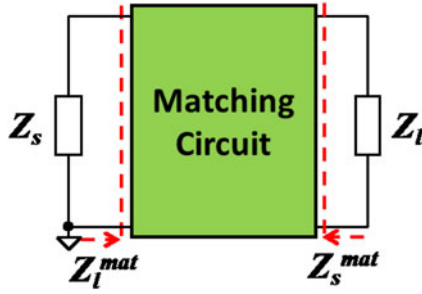


Fig. 4. Synthesized impedances Z_l^{mat} and Z_s^{mat} at two sides of the matching circuit.

condition, which is

$$Z_l^{mat} = Z_s^*. \quad (11)$$

And also the synthesized impedance Z_s^{mat} looking into the matching circuit and Z_s is satisfied with the matching condition, that is,

$$Z_s^* = Z_l^{mat}. \quad (12)$$

That means the matching circuit without ohmic loss can meet the matching conditions at both sides of the matching circuit simultaneously. However, this situation is changed when the matching circuit has the ohmic loss in its reactive components. If the B and X are obtained from the matching condition of $Z_l^{mat} = Z_s^*$ by our proposed method, the matching condition at other side $Z_l^* = Z_s^{mat}$ is not guaranteed again or vice versa due to the loss of matching circuit.

V. EXAMPLES

This section shows the simulation results of four different kinds of load impedances. The target impedance is set to 50Ω , and the load impedances are $100 + j50 \Omega$, $1 + j6 \Omega$, $4.47 - j1166 \Omega$, and $2.90 + j1686 \Omega$, respectively. Especially, $4.47 - j1166 \Omega$ is the impedance of electrically small transmitting dipole antenna of $2l = 0.1\lambda$ as used in WPT transmission model in Fig. 5, where the receiving antenna has the same dipole antenna structure loaded by 50Ω . $2.90 + j1686 \Omega$ is the input impedance of 20 turns coil

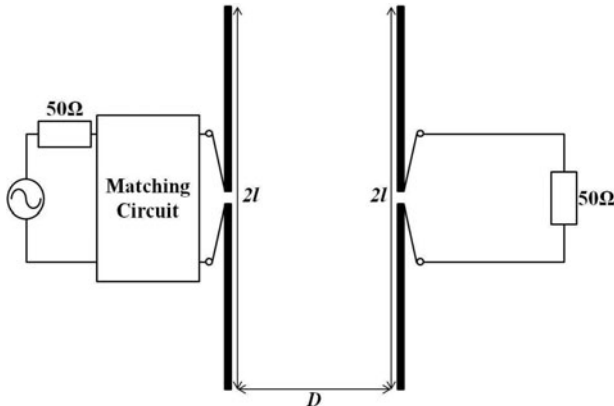


Fig. 5. An example of WPT system using small dipole antennas.

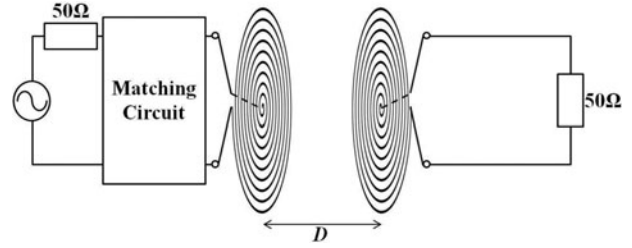


Fig. 6. An example of WPT system using 20 turns coils.

as used in WPT transmission model in Fig. 6, where the receiving antenna has the same coil structure loaded by 50Ω . The distances D in both dipoles and coils WPT systems are 0.1 wavelengths. Both impedances at transmitting side include the mutual coupling from the receiving elements.

Fig. 7 shows the positions of four kinds of load impedances on the Smith chart. $100 + j50 \Omega$ is located in area I, $1 + j6 \Omega$ is located in area II, $4.47 - j1166 \Omega$ and $2.90 + j1686 \Omega$ are located in area III if the ideal matching circuits without ohmic losses are applied. Four kinds of impedances represent the impedances in three areas, that is why we use them as the example load impedances. The impedance of WPT system where two dipoles are used has a very small resistance and a large negative reactance and is located in area III, while that of two coils has very small resistance and large positive reactance and also located in area III. Therefore, the matching design methods for dipoles and coils coupling will be very similar.

A) Lossless matching circuit design

Firstly, the CMA using ideally reactive components without the ohmic loss is used to design the matching circuits for the above four kinds of load impedances. The reactive components X and B of each matching circuit calculated by CMA are listed in Table 1. When $Z_l = 100 + j50 \Omega$, BX L-section matching circuit is used due to $G_l < G_s$. When $Z_l = 1 + j6 \Omega$, satisfying $R_l < R_s$, then XB L-section matching circuit is used. Both BX and XB L-section matching circuits are used as the matching circuits when $Z_l = 4.47 - j1166 \Omega$ and $Z_l = 2.90 + j1686 \Omega$, because $Z_l = 4.47 - j1166 \Omega$ and $Z_l = 2.90 + j1686 \Omega$ are in area III of the Smith Chart as shown in Fig. 2. The synthesized impedances Z_l^{mat} are equivalent to

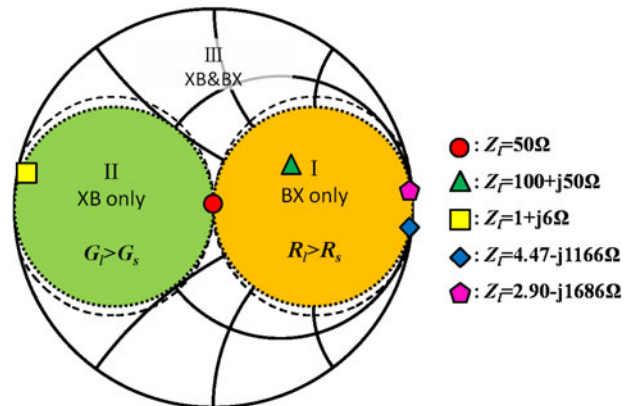


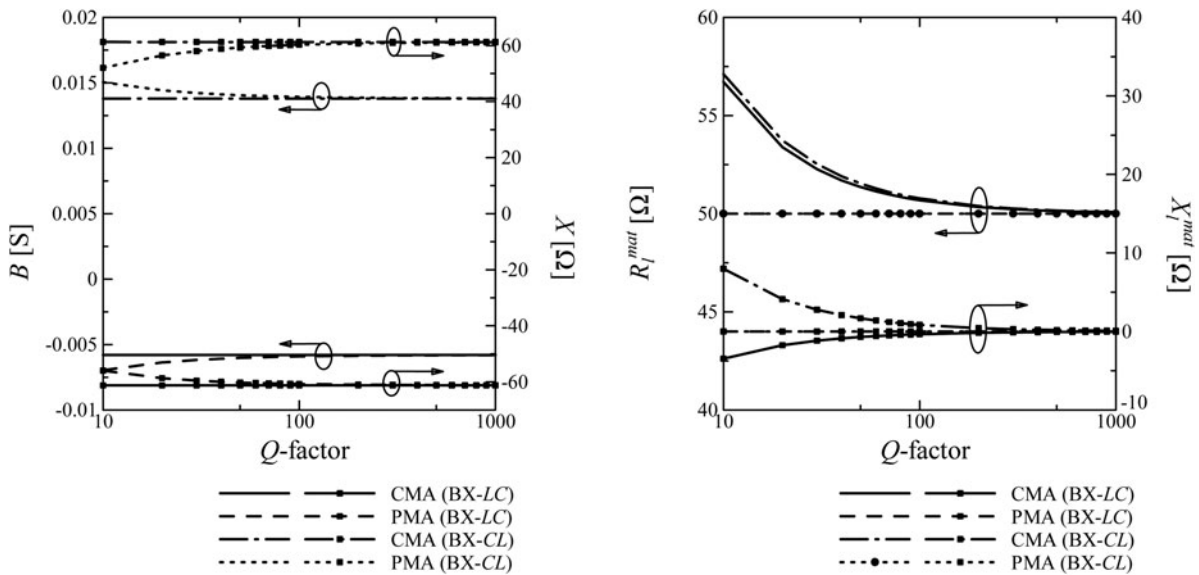
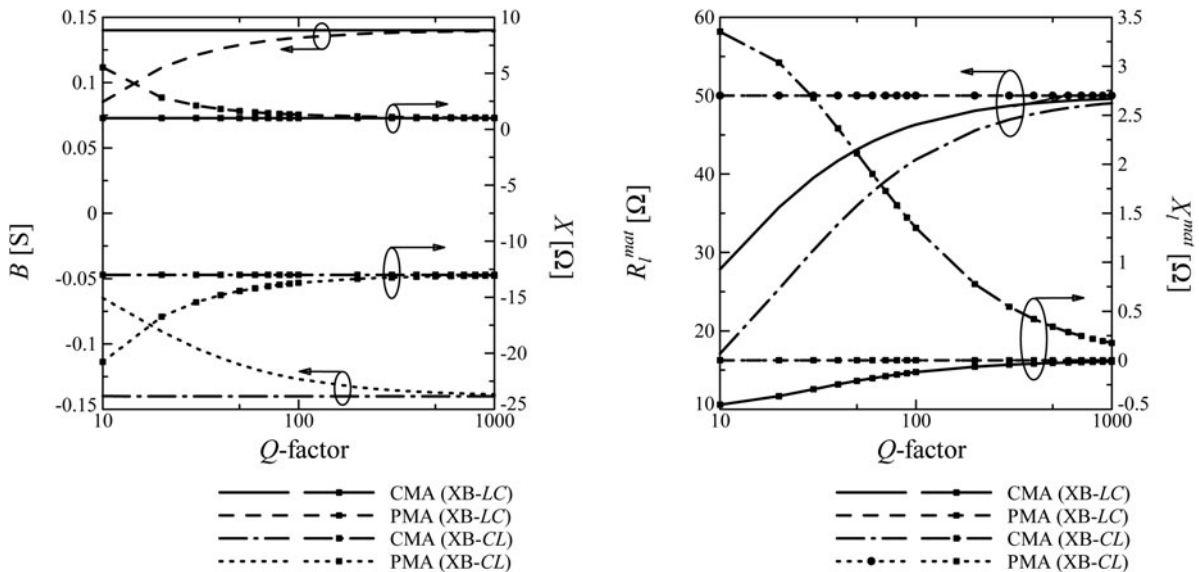
Fig. 7. The positions of load impedances in Smith Chart.

Table 1. Components of the L-section matching circuit without loss.

Z_l [Ω]	Topology type	B [S]	X [Ω]	Z_l^{mat} [Ω]
$100 + j50$	BX	0.0138	61.2	50.0
	BX	-0.00580	-61.2	50.0
$1 + j6$	XB	0.140	1.00	50.0
	XB	-0.140	-13.0	50.0
$4.47 - j1166$	BX	-0.000601	3900	50.0
	BX	-0.00111	-3900	50.0
	XB	0.0638	1180	50.0
	XB	-0.0638	1150	50.0
$2.90 + j1686$	BX	0.000736	7000	50.0
	BX	0.00045	-6990	50.0
	XB	-0.0806	-1700	50.0
	XB	0.0806	-1670	50.0

target impedance of 50Ω , and it represents that all matching circuits can get matched to the target impedance 50Ω by CMA.

As shown in Table 1, it can be observed that there are two selective values for B and X even with the same L-section topology BX or XB because equations (1)–(4) for B and X have both plus and minus solutions. For example, when $Z_l = 100 + j50 \Omega$, BX L-section matching circuit is applied, there are still two solutions for the elements. One solution uses Shunt C and series L as matching components with $B = 0.0138 \text{ S}$, $X = 61.2 \Omega$, and the other uses shunt L and series C as matching components with $B = -0.0058 \text{ S}$, $X = -61.2 \Omega$. In later part of this paper, the symbols BX-CL and BX-LC will be used for denoting the above two kinds of matching circuit.

**Fig. 8.** B , X and Z_l^{mat} versus Q -factor ($Z_l = 100 + j50 \Omega$, BX Topology).**Fig. 9.** B , X and Z_l^{mat} versus Q -factor ($Z_l = 1 + j6 \Omega$, XB Topology).

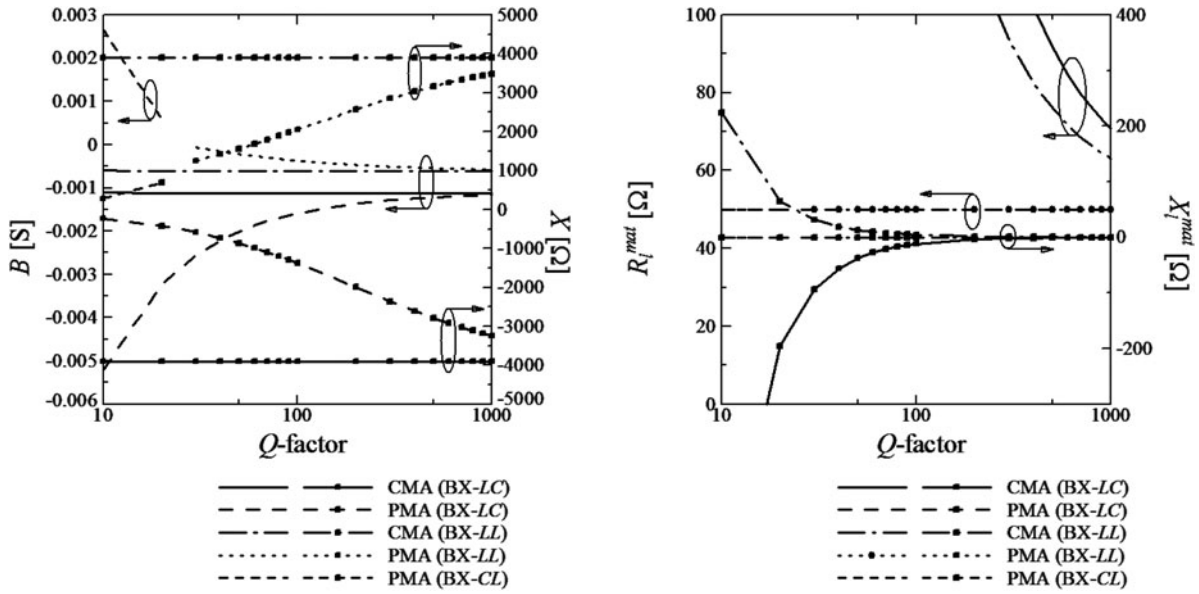


Fig. 10. B , X and Z_l^{mat} versus Q -factor ($Z_l = 4.47 - j1166 \Omega$, BX Topology).

B) Lossy matching circuit design

Here, the reactive components used in matching circuit will be treated as the practical ones, meaning the ohmic losses will be taken into account. Both CMA and PMA will be applied to design the L-section matching circuits. Certainly, the values of reactive components are calculated ignoring the ohmic loss when CMA is used.

The synthesized impedances $Z_l^{mat} = R_l^{mat} + jX_l^{mat}$ versus the Q factors by using the CMA and the PMA are compared in Figs 8–13, where $Q_X = Q_B$. Figure 8 is for $Z_l = 100 + j50 \Omega$, Fig. 9 is for $Z_l = 1 + j6 \Omega$, Figs 10 and 11 are for $Z_l = 4.47 - j1166 \Omega$, and Figs 12 and 13 are for $Z_l = 2.90 + j1686 \Omega$. As seen, Z_l^{mat} obtained by using the CMA does not equal to R_s except the case when Q -factor is larger than about 500, but all Z_l^{mat} obtained by using the PMA are equivalent to target impedance R_s for any Q -factor. Therefore, it is validated that

the PMA is an effective approach to design a matching circuit when the ohmic loss is considered. And it is also seen that the values of both reactance and susceptance obtained by PMA are approaching to the values obtained by CMA when both Q -factors are large enough. In Fig. 11, the XB topology can be only used when $Q \geq 30$. Because $4.47 - j1166 \Omega$ is located in area I when $Q < 30$, XB topology cannot be applied in this case. It makes that there are three types of BX topologies obtained by PMA in Fig. 10 for $4.47 - j1166 \Omega$ due to the same reason. Similarly, it happens on coil types as shown in Figs 12 and 13.

Tables 2 and 3 show the values of inductor and capacitor of the matching circuits to $4.47 - j1166 \Omega$ where two electrically small dipoles are used as shown in Fig. 5. Table 2 shows four types of matching circuits consisting of one inductor and one capacitor, and Table 3 shows four types of matching circuits

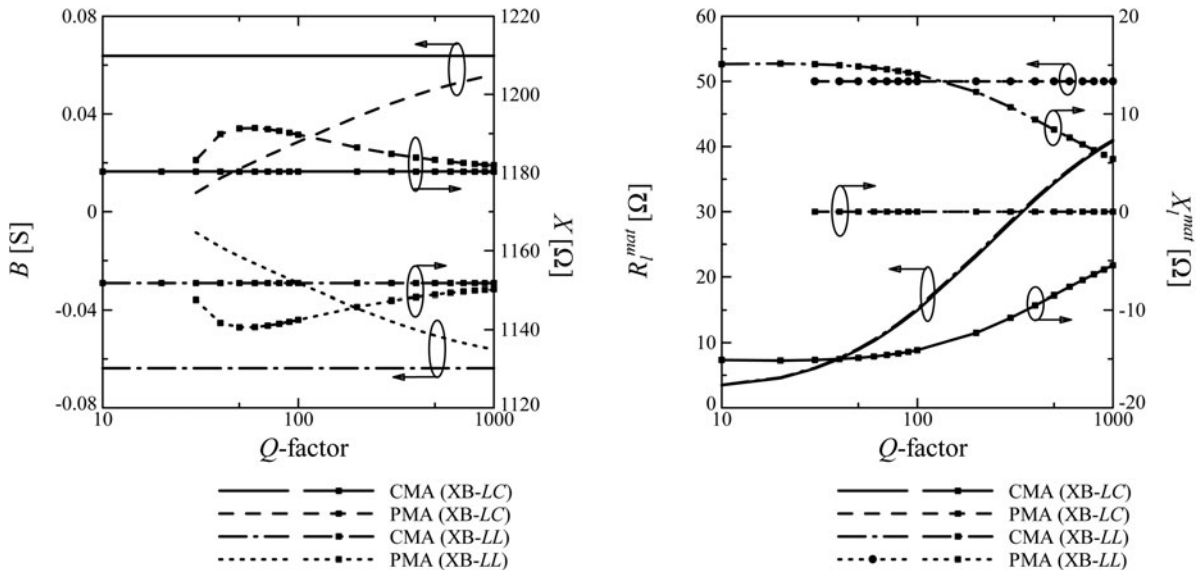


Fig. 11. B , X and Z_l^{mat} versus Q -factor ($Z_l = 4.47 - j1166 \Omega$, XB Topology).

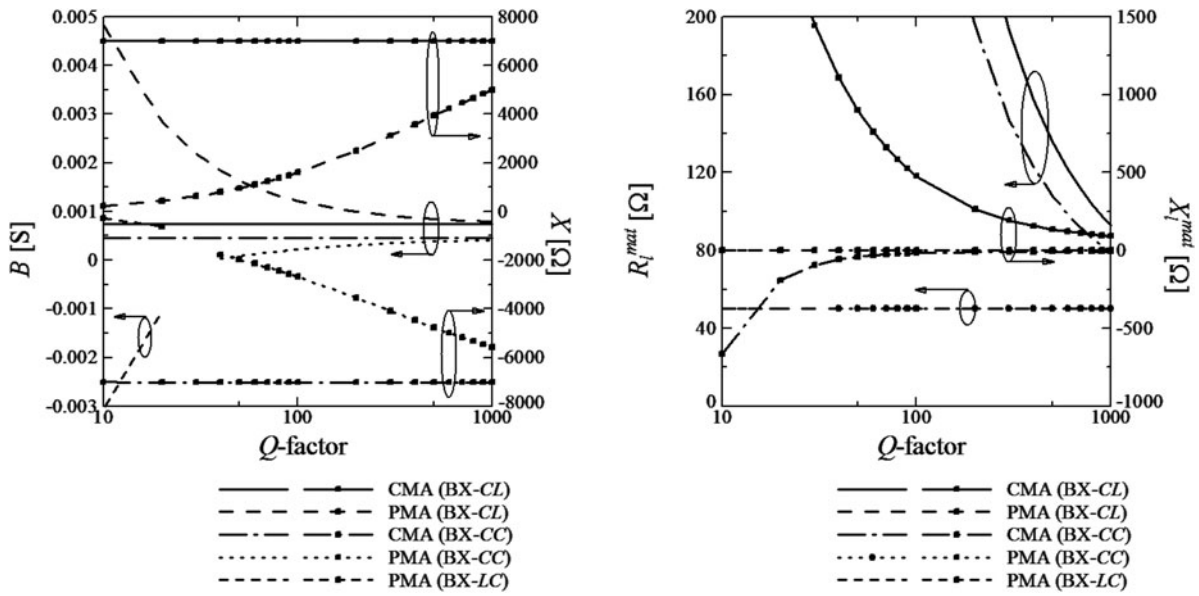


Fig. 12. B , X and Z_l^{mat} versus Q -factor ($Z_l = 2.90 + j1686 \Omega$, BX Topology).

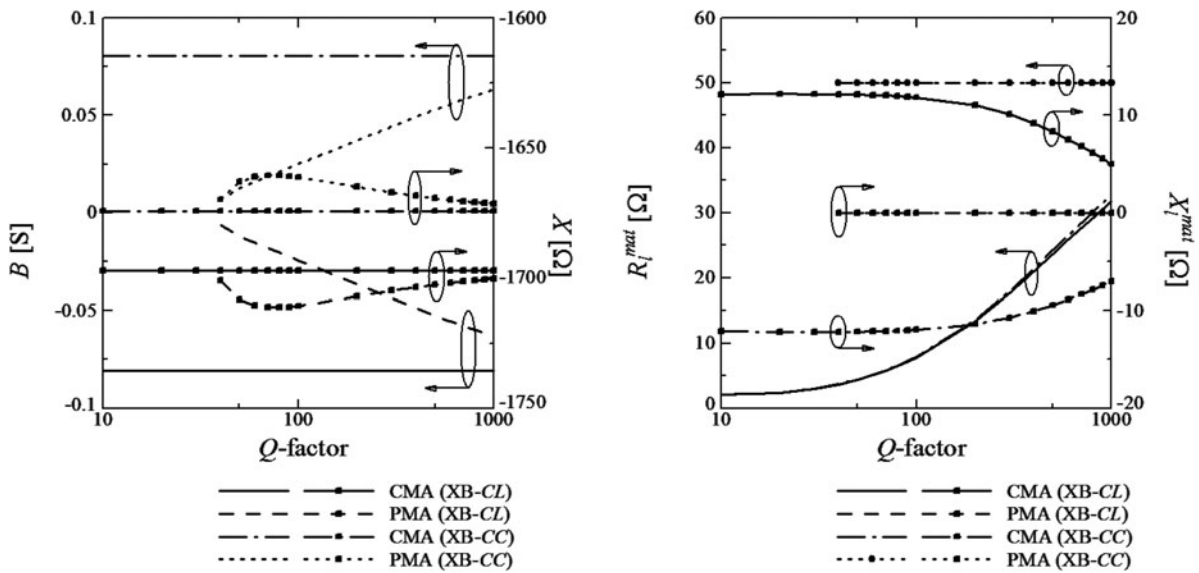


Fig. 13. B , X and Z_l^{mat} versus Q -factor ($Z_l = 2.90 + j1686 \Omega$, XB Topology).

consisting of two inductors. Tables 4 and 5 show the values of inductor and capacitor of the matching circuits to $2.90 + j1686 \Omega$ where the 20 turns helical coils are used as shown in Fig. 6. Table 4 shows four types of matching circuits consisting of one inductor and one capacitor, and Table 5 shows four types of matching circuits consisting of two

capacitors. For Tables 2–5, the frequency is set to be 6.78 MHz. And from practical datasheets of chip elements, Q -factors of inductor and capacitor are set to be 30 and 1000, respectively.

From the above tables, it has been demonstrated that CMA cannot design the correct matching circuit to meet the

Table 2. Impedance matching circuits consisting of inductor and capacitor for dipole antenna.

Matching method	Z_a [Ω]	Topology type	L [H]	C [pF]	Z_l^{mat} [Ω]
CMA	$4.47 - j1166$	BX	21.1	6.02	$604 - j94$
		XB	27.7	1500	$5.61 - j15.5$
PMA	$4.47 - j1166$	BX	73.4	12.6	50.0
		XB	27.8	175	50.0

Table 3. Impedance matching circuits consisting of two inductors for dipole antenna.

Matching method	Z_a [Ω]	Topology type	Series L [H]	Parallel L [H]	Z_l^{mat} [Ω]
CMA	$4.47 - j1166$	BX	91.5	39.0	$482 + j31.4$
		XB	27.0	0.368	$6.14 + j15.1$
PMA	$4.47 - j1166$	BX	29.4	397	50.0
		XB	26.9	2.75	50.0

Table 4. Impedance matching circuits consisting of capacitor and inductor for 20 turns coil.

Matching method	Z_a [Ω]	Topology type	C [pF]	L [H]	Z_l^{mat} [Ω]
CMA	$2.90 + j1686$	BX	17.3	164	$318 + j36.3$
		XB	13.8	0.291	$25.2 + j23.4$
PMA		BX	311	32.1	50.0
		XB	13.8	0.392	50.0

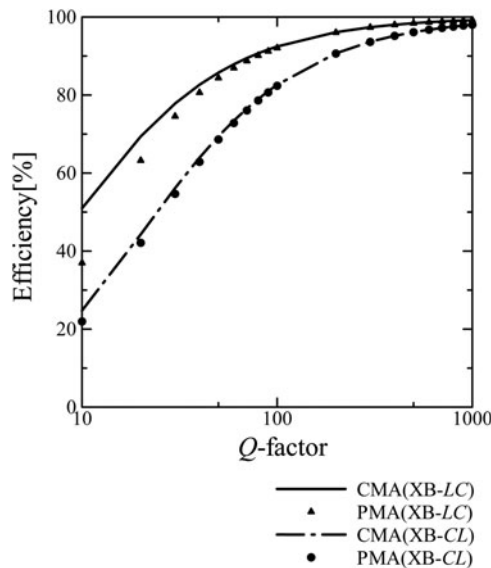
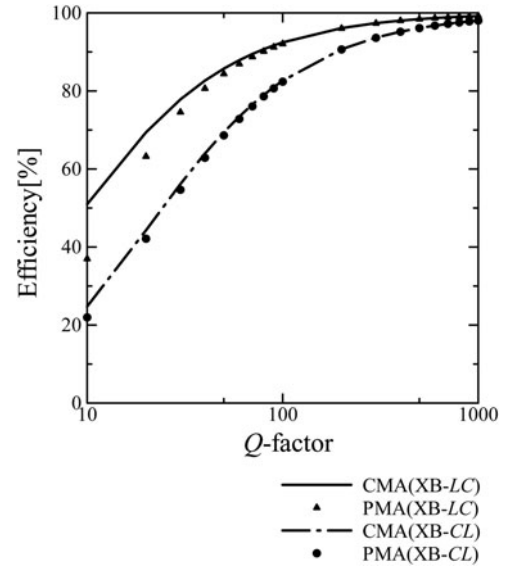
Table 5. Impedance matching circuits consisting of two capacitors for 20 turns coil.

Matching method	Z_a [Ω]	Topology type	Series C [pF]	Parallel C [pF]	Z_l^{mat} [Ω]
CMA	$2.90 + j1686$	BX	3.35	10.6	$80.4 + j56.3$
		XB	14.0	1890	$24.3 - j27.4$
PMA		BX	4.22	9.71	50.0
		XB	140	1480	50.0

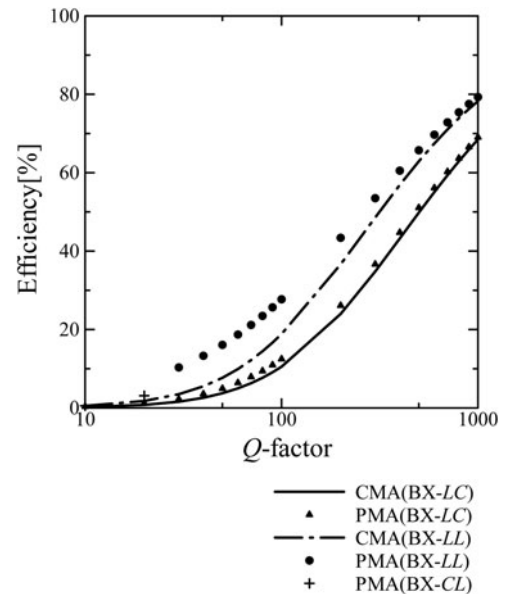
matching condition for electrically small dipole antenna and coils coupling WPT system. However, PMA can design the correct matching circuit.

C) Transmission efficiencies versus reactive component Q-factors of L-section matching circuit

Furthermore, the transmission efficiencies versus Q-factor with L-section matching circuit for $Z_l = 100 + j50 \Omega$ and $1 + j6 \Omega$ are plotted in Figs 14 and 15. The efficiency in this paper is defined as P_l/P_a , where P_l denotes the power consumed at Z_l and P_a is the available maximum power input into the matching circuit. In [3, 4, 18], the optimal impedances at both sides for maximum transfer efficiency were deduced


Fig. 14. Efficiency versus Q-factor ($Z_l = 100 + j50 \Omega$, BX Topology).

Fig. 15. Efficiency versus Q-factor ($Z_l = 1 + j6 \Omega$, XB Topology).

and included in the mutual coupling effect between the transmitting and receiving sides. The main purpose of this paper is how to realize these two optimal impedances at both transmitting and receiving sides, which is how to design matching circuit between two arbitrary impedances. So the efficiency discussed in this paper is limited to the power delivery between two impedances, not between the transmitting and receiving elements. The efficiency defined in [3, 4, 18] is equivalent to the transducer power gain as defined in [5, 20]. For the case of $Z_l = 100 + j50 \Omega$, two types of BX L-sections as shown in Fig. 14 are available as the matching circuits. The efficiencies of shunt L and series C are better than shunt C and series L for both lossless and lossy design approaches. For the case of $Z_l = 1 + j6 \Omega$, two types of XB L-sections as shown in Fig. 15 are available as the matching circuits. The efficiencies of series L and shunt C matching


Fig. 16. Efficiency versus Q-factor ($Z_l = 4.47 - j1166 \Omega$, BX Topology).

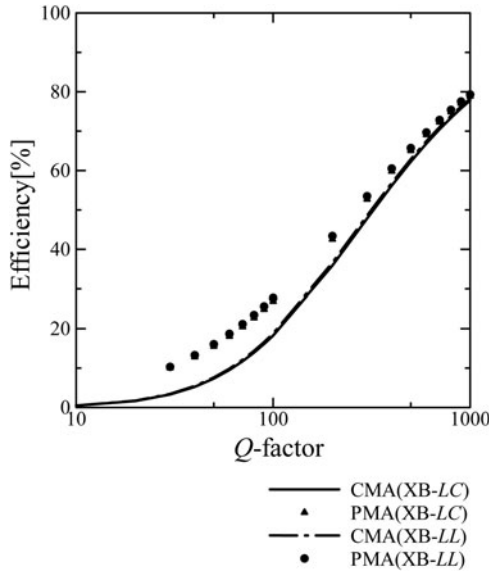


Fig. 17. Efficiency versus Q -factor ($Z_l = 4.47 - j1166 \Omega$, XB Topology).

circuit are better than those of series C and shunt L circuit for both CMA and PMA. The efficiencies with PMA are less than those with CMA.

For the case of $4.47 - j1166 \Omega$, three types of BX L-sections and two types of XB L-sections are available as the matching circuits in PMA. In the BX topology, the efficiencies of shunt L and series L circuit are better than those of shunt L and series C circuit. At $Q_X = Q_B = 30$, shunt L and series L circuit is replaced by shunt C and series L circuit due to the changing of the relationship between Z_l and Z_s in PMA. In the XB topology, the efficiencies of series L and shunt L are better than those of series L and shunt C circuit for both methods, designed either by CMA or PMA. For the case of $2.90 + j1686 \Omega$, three types of BX L-sections and two types of XB L-sections are available as the matching circuits in PMA. In the BX topology, the efficiencies of shunt C and series C circuit are better than those of shunt C and series L

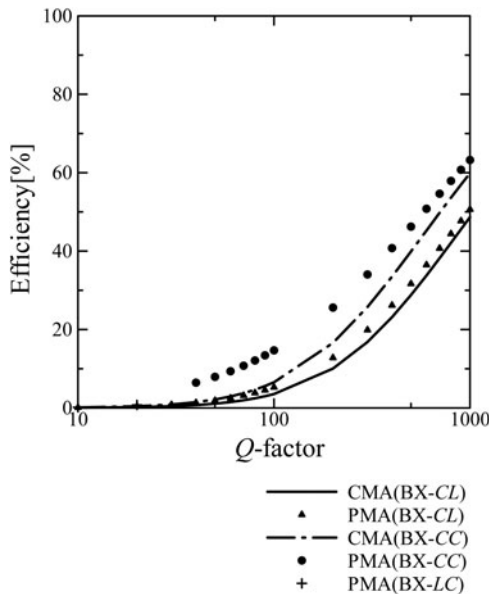


Fig. 18. Efficiency versus Q -factor ($Z_l = 2.90 + j1686 \Omega$, BX Topology).

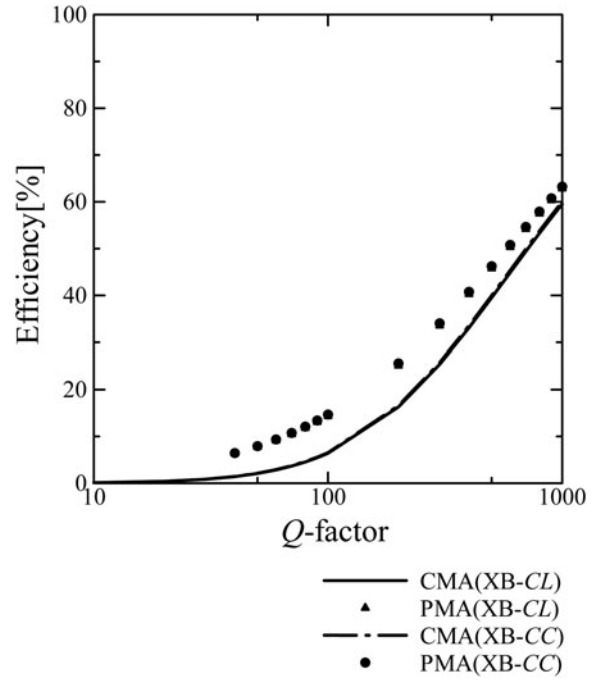


Fig. 19. Efficiency versus Q -factor ($Z_l = 2.90 + j1686 \Omega$, XB Topology).

circuit. At $Q_X = Q_B = 40$, shunt C and series C circuit is replaced by shunt L and series C circuit due to the changing of the relationship between Z_l and Z_s in PMA. In the XB topology, the efficiencies of series C and shunt C are better than those of series C and shunt L circuit for both methods, designed either by CMA or PMA. Different from the cases when $Z_l = 100 + j50 \Omega$ and $Z_l = 1 + j6 \Omega$, the efficiencies by using the PMA is higher than those by using the CMA.

Comparing with CMA, the efficiency reduction by PMA for the cases of $Z_l = 100 + j50 \Omega$ and $Z_l = 1 + j6 \Omega$ results from the requirement of the larger B or X which brings with the larger ohmic loss. However, the PMA does improve the total deficiency for the practical cases when load impedance are $4.47 - j1166 \Omega$ and $2.90 + j1686 \Omega$. For the matching circuit without ohmic loss, the efficiency between two arbitrary impedances will be the maximum one according to conjugate matching condition, and it gives the higher efficiency obtained by PMA when ohmic loss is taken into account for the impedance with very small resistance and large reactance. However, the efficiency between two arbitrary impedances by PMA is not the highest one. How to obtain the highest one for the efficiency between two arbitrary impedances and also WPT system at the presence of the ohmic loss in the matching circuit will be our next research topics (Figs 16–19).

VI. CONCLUSIONS

An exact method to design L-section lumped impedance matching circuit with elements' ohmic losses has been proposed in this paper. It has been confirmed that the proposed method is effective for any two different impedances to satisfy the complex conjugate matching condition. The effects of ohmic loss and mismatching on the matching circuit efficiency have been investigated by some examples including two practical models when two electrically small dipole antennas and two coils are used as transmitting and

receiving elements in WPT system. And it has been found that the improvement on the efficiency by using the proposed exact matching circuit approach is achieved when the impedance has very low resistance and the large reactive part which usually happens on an electrically small dipole or coils. Additionally, in this paper, the following novel results have been obtained.

- (1) The perfect selection topology conditions have been established and very easy to be applied to any cases when ohmic losses do not exist. And the modification of the conditions for lossy matching circuit has been also proposed.
- (2) It has been confirmed that the matching condition is satisfied at only one side of the matching circuit when the impedance matching circuit has ohmic losses. Therefore, the complex conjugate matching condition does not guarantee to achieve the maximum transmission efficiency of the matching circuit if the losses of lumped elements are considered.

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REFERENCES

- [1] Simon, O.; Mahlein, J.; Turki, F.; Dörflinger, D.; Hoppe, A.: Field test results of interoperable electric vehicle wireless power transfer, in 2016 18th European Conf. on Power Electronics and Applications (EPE'16 ECCE Europe), Karlsruhe, 2016, 1–10.
- [2] Kracek, J.; Svanda, M.; Mazanek, M.; Machac, J.: Semi-active 866 MHz RFID implantable tag fed by 6.78 MHz inductive wireless power transfer, in 2016 46th European Microwave Conf. (EuMC), London, 2016, 620–622.
- [3] Yuan, Q.; Chen, Q.; Sawaya, K.: Numerical analysis on transmission efficiency of evanescent resonant coupling wireless power transfer system. *IEEE Trans. Antennas Propag.*, **58** (5) (2010), 1751–1758.
- [4] Chen, Q.; Ozawa, K.; Yuan, Q.; Sawaya, K.: Antenna characterization for wireless power-transmission system using near-field coupling. *IEEE Antennas Propag. Mag.*, **54** (4) (2012), 108–116.
- [5] Rahola, J.: Power waves and conjugate matching. *IEEE Trans Circuits Syst II Express Briefs*, **55** (1) (2008), 92–96.
- [6] Rollett, J.M.: Stability and power gain invariants of linear two ports. *IRE Trans. Circuit Theory*, **9** (1962), 29–32.
- [7] Inagaki, N.: Theory of image impedance matching for inductively coupled power transfer systems. *IEEE Trans. Microw. Theory Tech.*, **62** (4) (2014), 901–908.
- [8] Roberts, S.: Conjugate-image impedances. *Proc. IRE*, **34** (4) (1946), 198–204.
- [9] Dionigi, M.; Mongiardo, M.; Perfetti, R.: Rigorous network and full-wave electromagnetic modeling of wireless power transfer links. *IEEE Trans. Microw. Theory Tech.*, **63** (1) (2015), 65–75.
- [10] Yuan, Q.; Niizeki, R.: Calculator of WPT Efficiency between Transmitting and Receiving elements,” in (invited) 2017 The Applied Computational Electromagnetics Society (ACES), August 2017.
- [11] Moon, J.; Hwang, H.; Jo, B.; Kwon, C.K.; Kim, T.G.; Kim, S.W.: Design and implementation of a high-efficiency 6.78 MHz resonant wireless power transfer system with a 5W fully integrated power receiver. *IET Power Electron.*, **10** (5) (2017), 577–587.
- [12] Shinki, Y.; Shibata, K.; Mansour, M.; Kanaya, H.: High-efficiency energy harvesting circuit with impedance matched antenna, in 2016 IEEE 18th Electronics Packaging Technology Conf. (EPTC), Singapore, 2016, 532–535.
- [13] Kim, J.; Kim, D.H.; Park, Y.J.: Analysis of capacitive impedance matching networks for simultaneous wireless power. *Transf. Multiple Devices*, **62** (5) (2015), 2807–2813.
- [14] Suzuki, S.; Abe, S.; Yuan, Q.: Efficiency of rectify circuit with matching circuit for wireless power transfer. *IEICE Tech. Rep.*, **114** (246) (2014), 11–14, WPT2014-37.
- [15] Rahola, J.: Optimization of matching circuits for antennas, in Proc. of the 5th European Conf. on Antennas and Propagation (EUCAP), Rome, 2011, 776–778.
- [16] Pozar, D.M.: *Microwave Engineering*, 4th ed. *Wiley International Edition*, 2011.
- [17] Yuan, Q.: Effect of loss of matching circuit on efficiency of wireless power transfer system. *IEICE Tech. Rep.*, **114** (375) (2014), 31–34, WPT2014-69.
- [18] Chen, Q.; Yuan, Q.: Antennas in wireless charging systems, in *Handbook of Antenna Technologies*, 2015, 1–24.
- [19] Yuan, Q.; Suzuki, S.: Exact matching approach with circuit element ohmic loss, in 2016 International Symp. on Antennas and Propagation (ISAP), October 2016, 342–343.
- [20] Collin, R.E.: *Foundations for Microwave Engineering*, 2nd ed., Wiley-Interscience, Singapore, 2001.



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